

Fig. 1

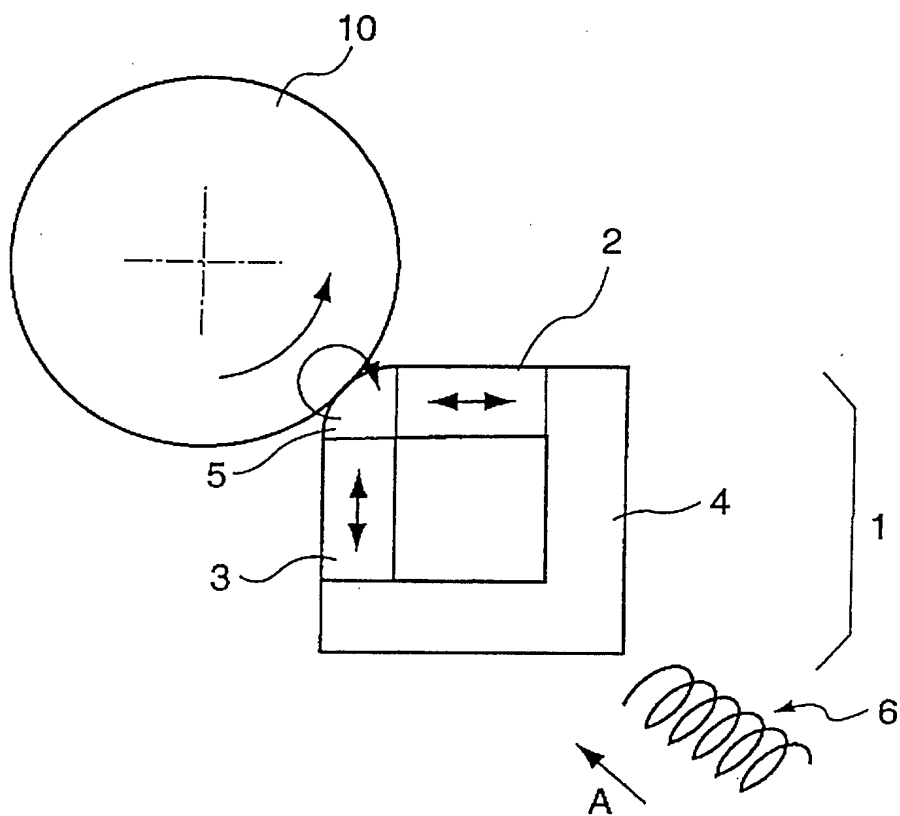


Fig. 2
(a)

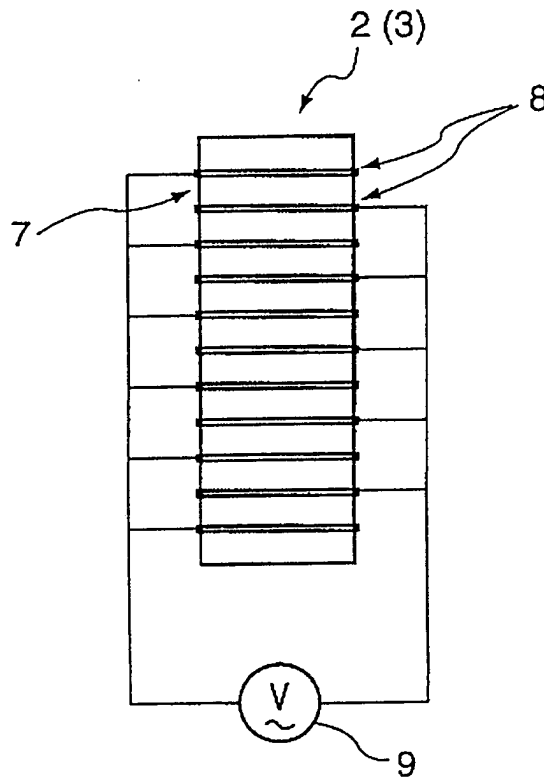


Fig. 2
(b)

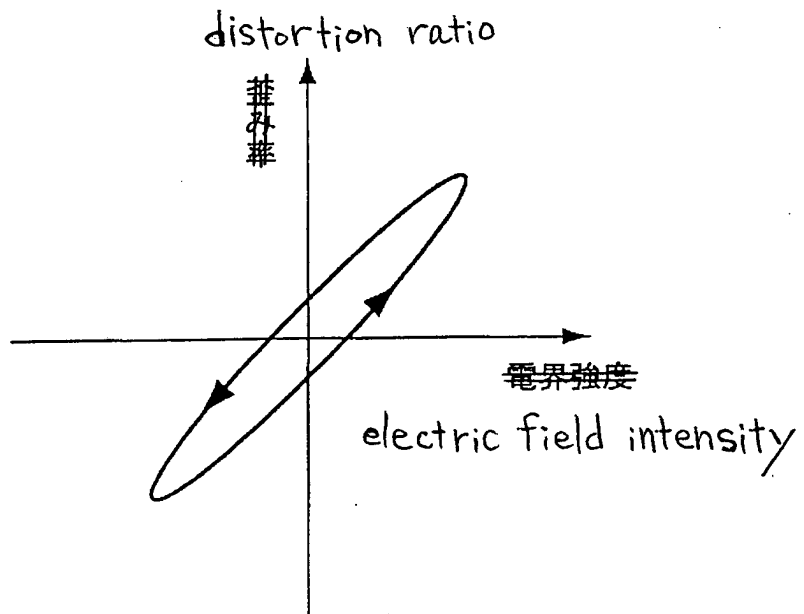


Fig. 3
(a)

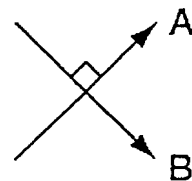
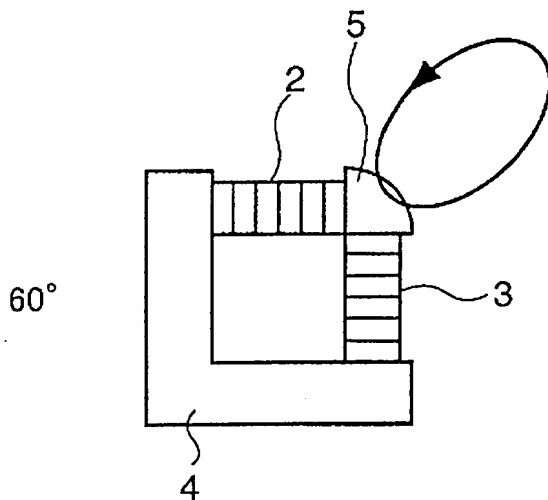


Fig. 3
(b)

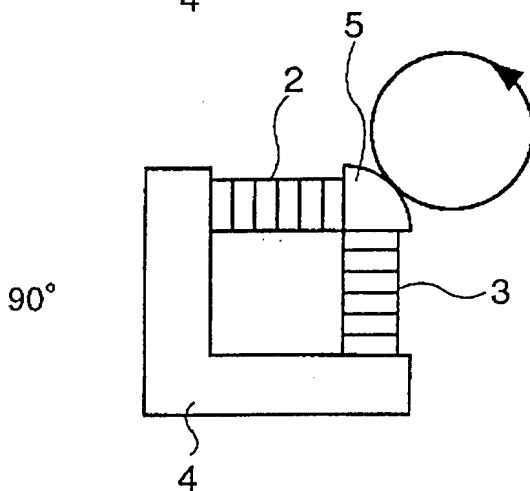


Fig. 3
(c)

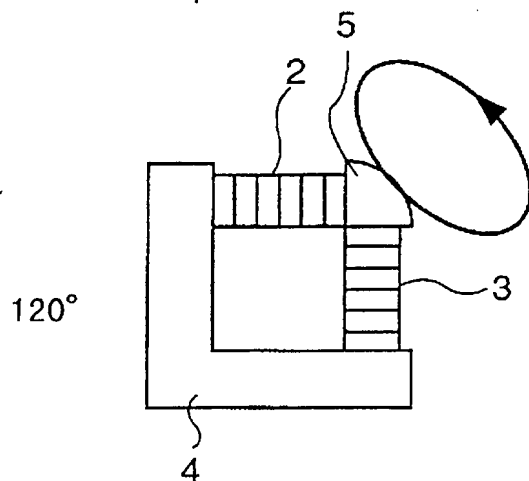
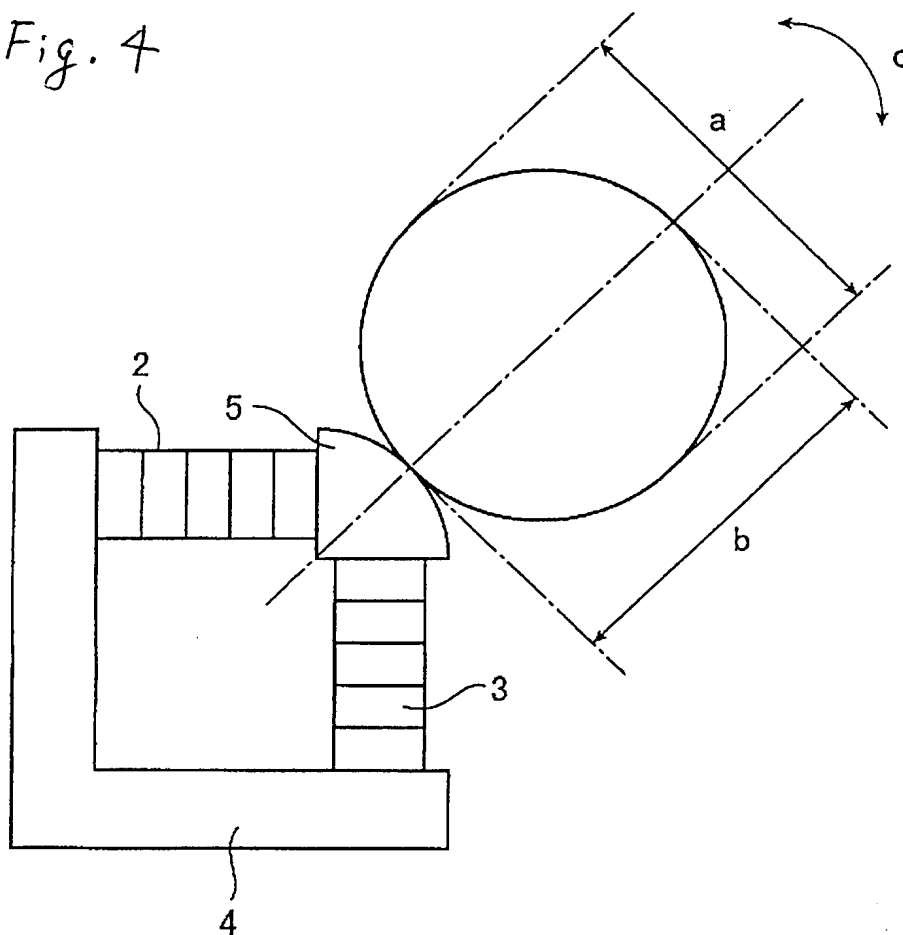


Fig. 4



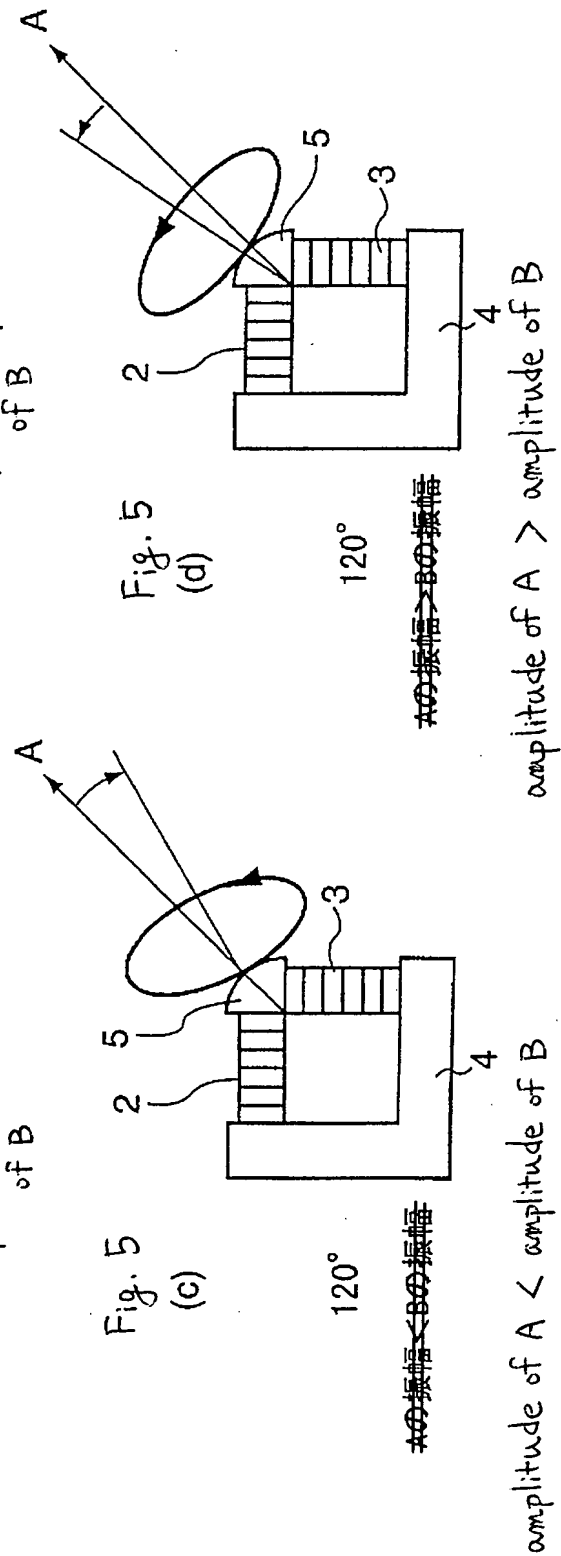
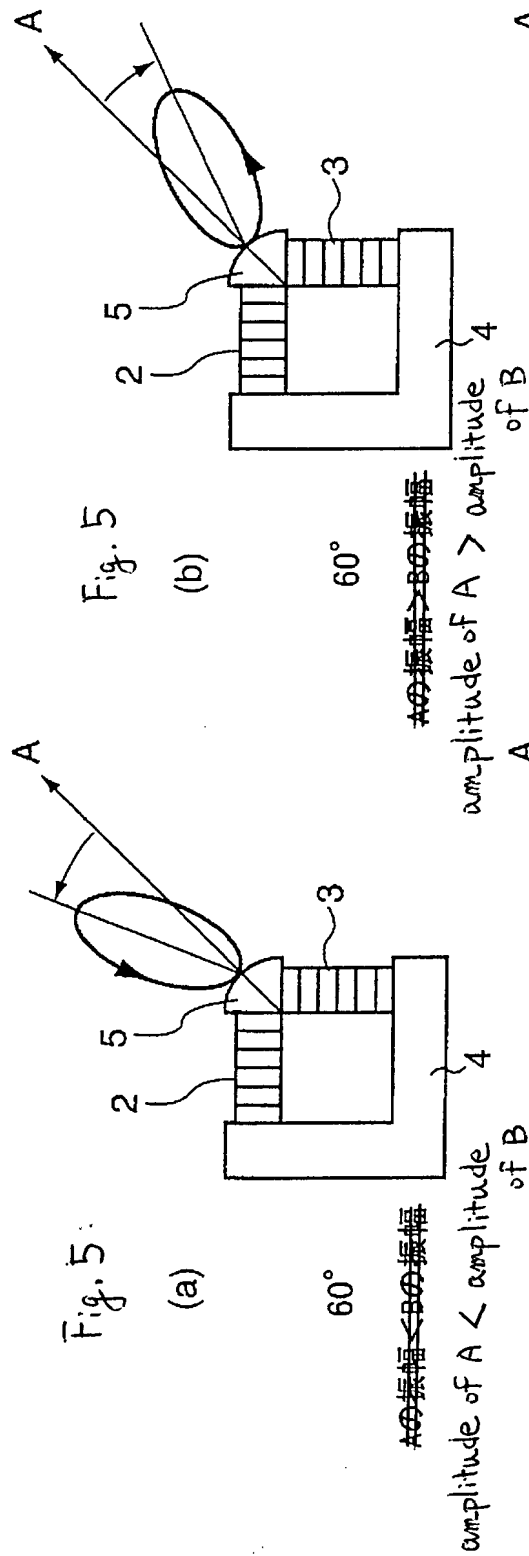


Fig. 6

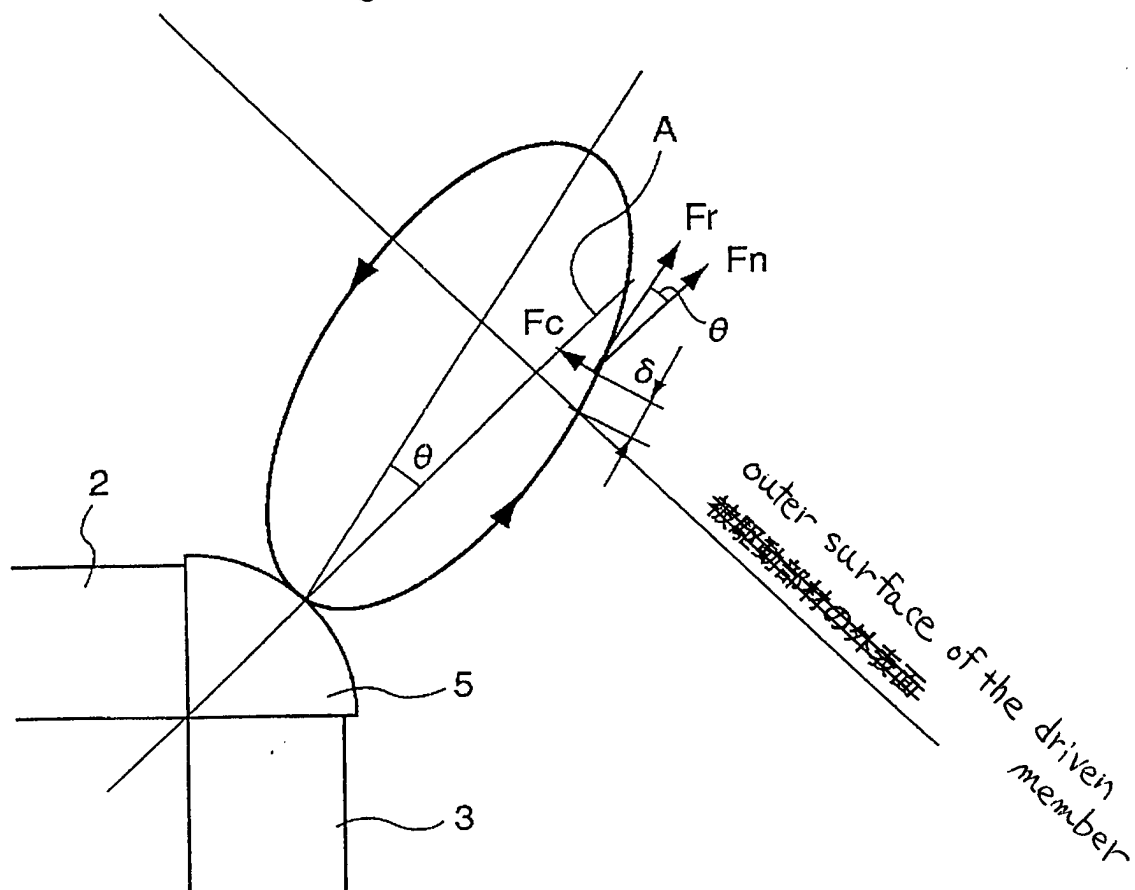


Fig. 7

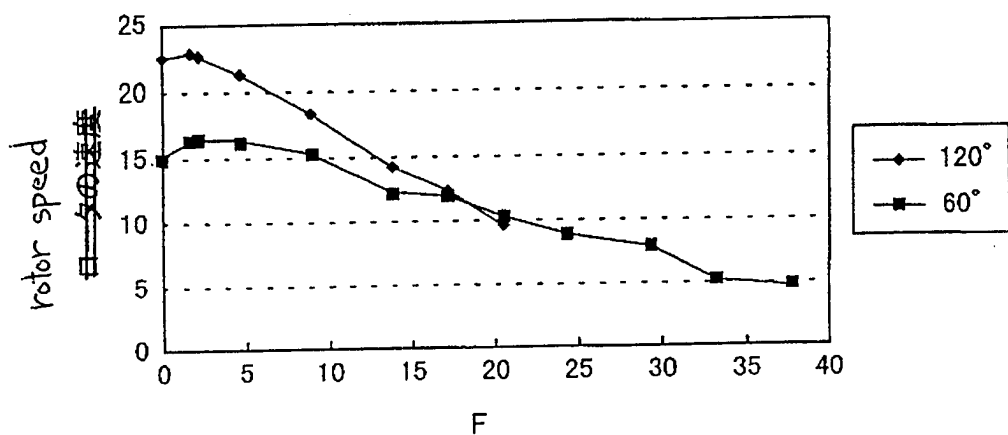


Fig. 8

configuration of locus
軌跡形状 (単位 μm)

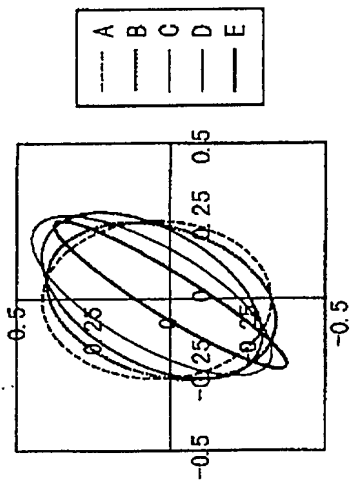


Fig. 8

(c)

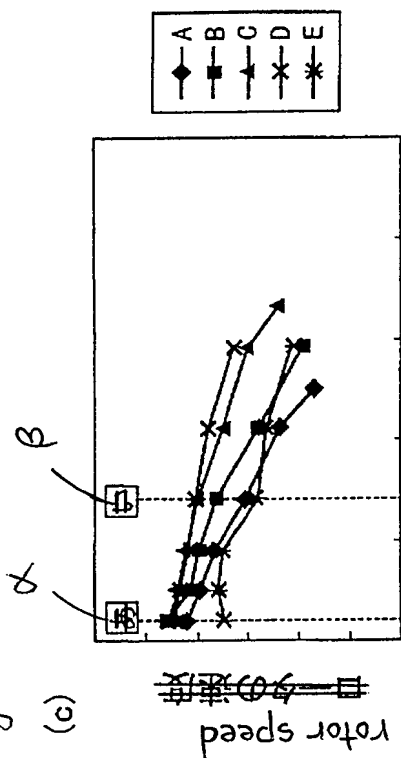


Fig. 8 (b) configuration of locus
軌跡形状 (単位 μm)

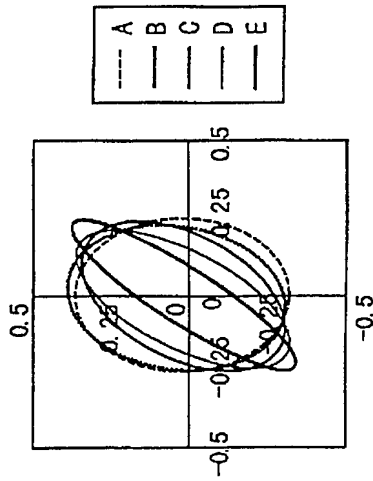


Fig. 8

(d)

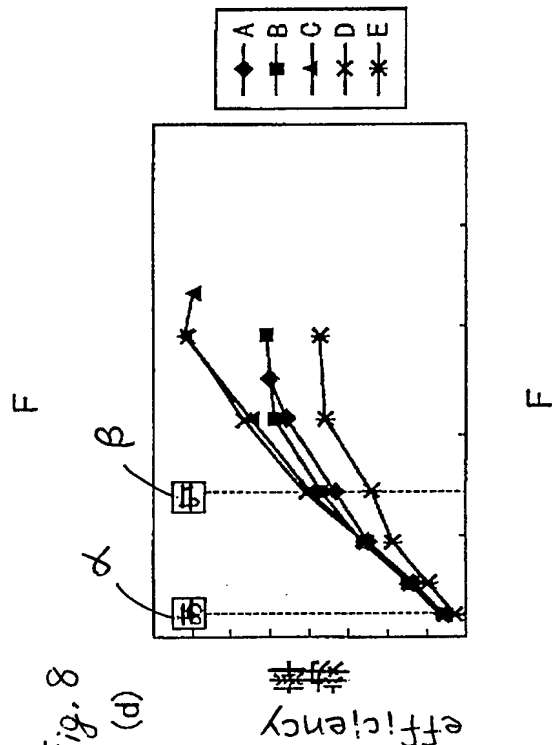


Fig. 11

(a)

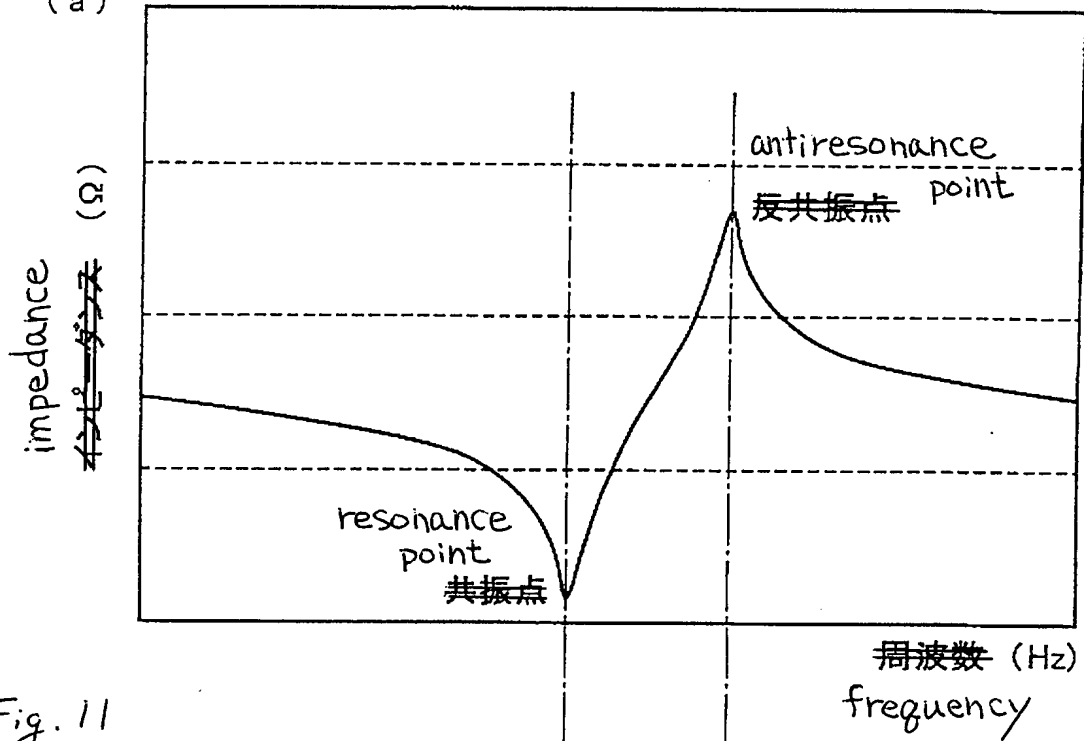
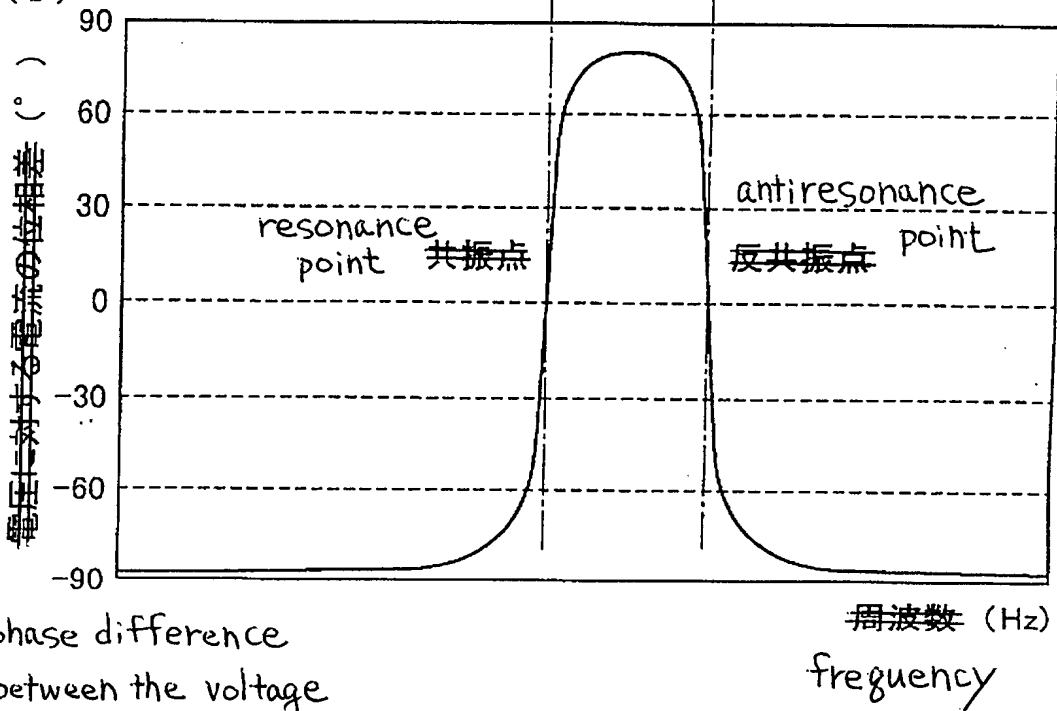


Fig. 11

(b)



phase difference
between the voltage
and the current

Fig. 14

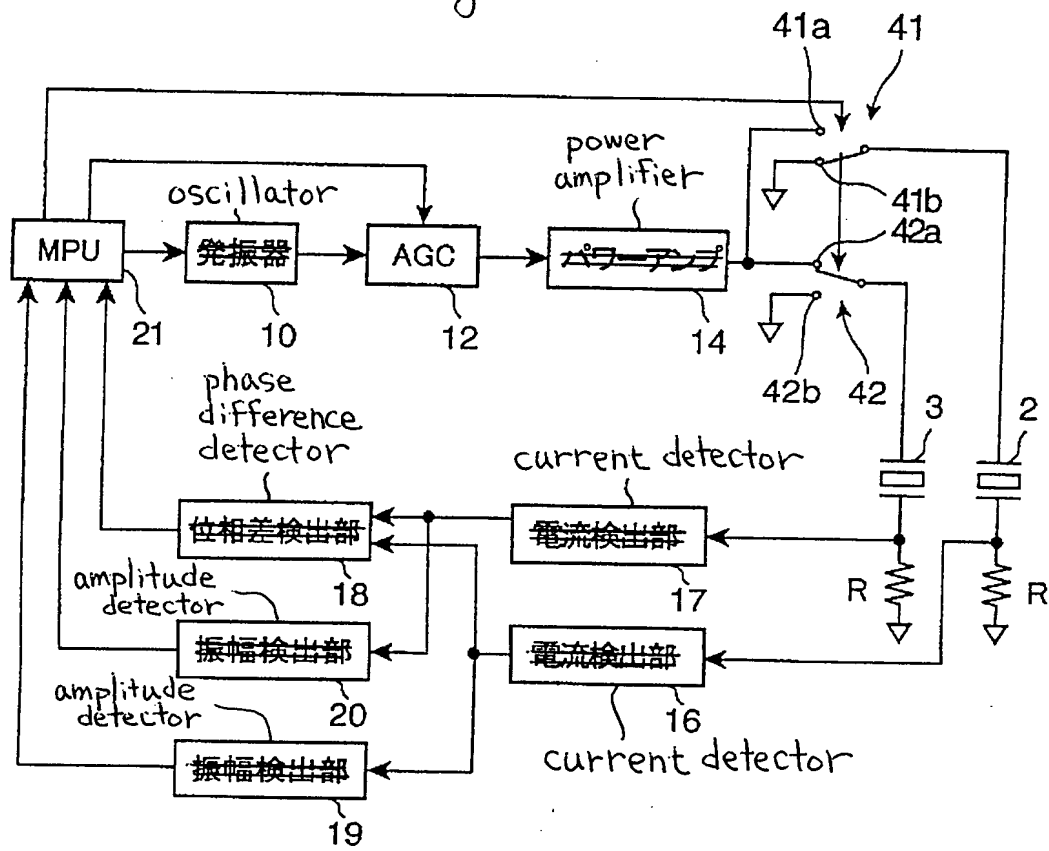


Fig. 15

